

IN THE CLAIMS

Please amend the claims as follows:

1. (Currently Amended) ~~Method~~ A method for generating a channel code with DC control, said method comprising the steps of:

- converting a stream of P n-bit input words into a stream of P m-bit code words; and

5 - converting the stream of P m-bit code words into an output stream of P m-bit output words using a NRZI converter, characterized in that the method further comprises the ~~following~~ steps of:

- determining a running digital sum of the output stream of
10 output words; and

- in response to the running digital sum, replacing a sequence of Q m-bit code words, by a replacement sequence of Q m-bit replacement code words, the replacement sequence being equally long and having a different parity than the sequence of code words

15 ~~it replaces~~ being replaced, and said replacement sequence never occurring in a stream of m-bit code words when converting any stream of n-bit input words into m-bit code words,

and in that the replacement sequence is chosen from the following table:

20 1: 101 001 010 100 -> 100 100 100 100
2: 010 001 000 101 -> 010 000 000 101

	<u>3: 001 001 000 101 -> 001 000 000 101</u>
	<u>4: 101 000 010 010 -> 100 100 000 010</u>
	<u>5: 101 001 000 001 -> 100 100 000 001</u>
25	<u>6: 101 000 100 101 -> 101 000 000 101</u>
	<u>7: 101 000 100 010 -> 101 000 000 010.</u>

2. (Currently Amended) ~~Method~~ The method as claimed in claim 1, characterized in that the method is employed after a further method of DC control.

3. (Currently Amended) ~~Method~~ The method as claimed in claim 2, characterized in that each method determines the running digital sum taking into consideration effects of the other method in the running digital sum.

4. (Currently Amended) ~~Method~~ The method as claimed in claim 1, characterized in that converting a stream of P n-bit words into a stream of P m-bit code words is achieved with a parity preserving coder.

5. (Currently Amended) ~~Method~~ The method as claimed in claim 1, characterized in that Q is equal to or larger than 4.

6. (Currently Amended) ~~Method~~ The method as claimed in claim 1, characterized in that a code constraint of the channel code is preserved.
7. (Currently Amended) ~~Method~~ The method as claimed in claim 4, characterized in that a 17PP coder performs the conversion from n-bit input words into a stream of m-bit words.
8. (Cancelled).
9. (Currently Amended) ~~Coding~~ A coding device using the method as claimed in claim 1.
10. (Currently Amended) ~~Recording~~ A recording device comprising the coding device as claimed in claim 9.
11. (Currently Amended) ~~Signal~~ A signal comprising a stream of code word obtained using the method as claimed in claim 1.
12. (Currently Amended) ~~Record~~ A record carrier comprising a the signal as claimed in claim 11.
13. (Currently Amended) ~~Reading~~ a reading device for reading a the record carrier as claimed in claim 12.